

CM35MX-24A

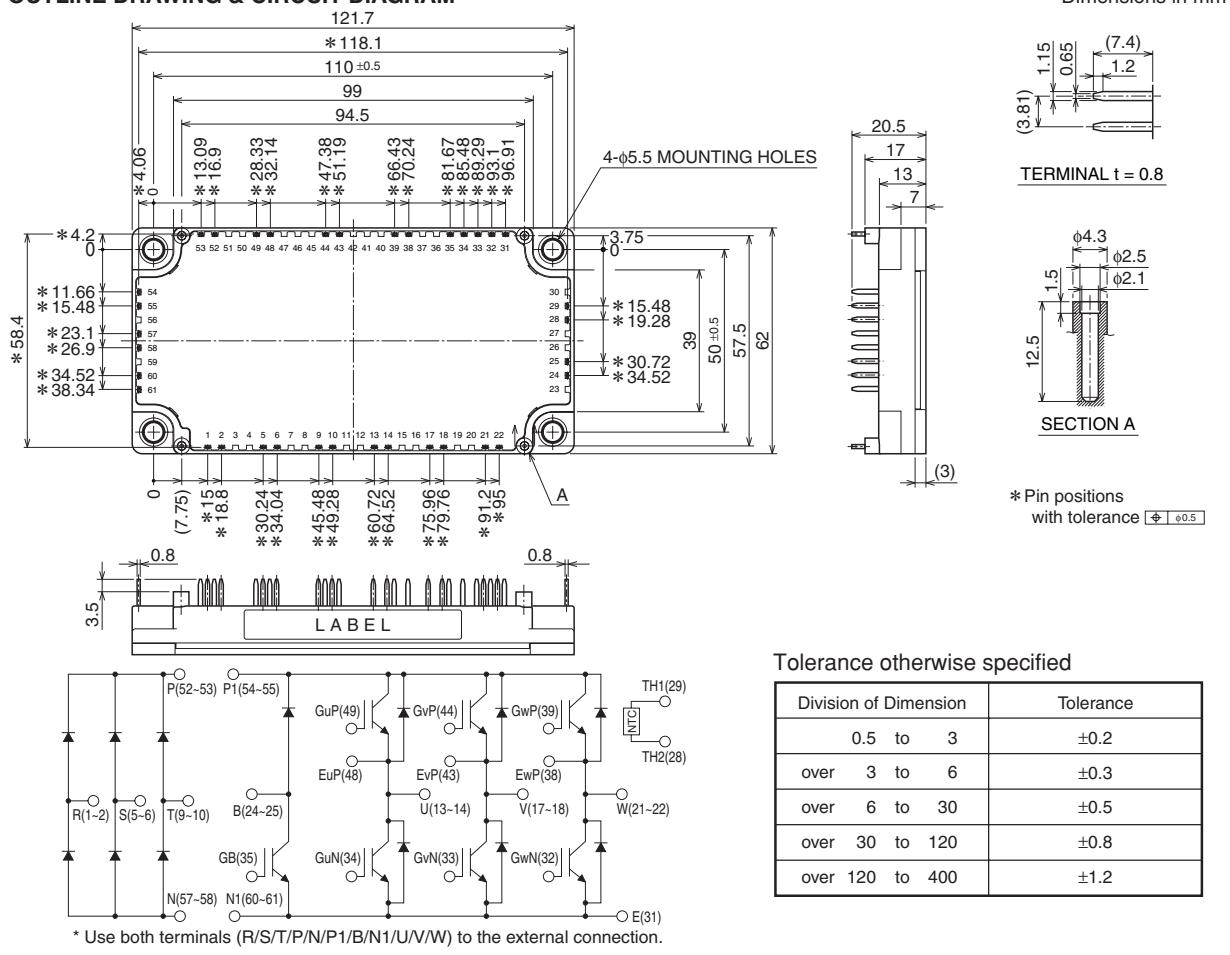
HIGH POWER SWITCHING USE

CM35MX-24A

- IC 35A
- V_{CE(S)} 1200V
- CIB (3-phase Converter + 3-phase Inverter + Brake)
- Flatbase Type / Insulated Package / Copper base plate
- RoHS Directive compliant

APPLICATION

General purpose Inverters, Servo Amplifiers

OUTLINE DRAWING & CIRCUIT DIAGRAM

Tolerance otherwise specified

Division of Dimension	Tolerance
0.5 to 3	±0.2
over 3 to 6	±0.3
over 6 to 30	±0.5
over 30 to 120	±0.8
over 120 to 400	±1.2

HIGH POWER SWITCHING USE**ABSOLUTE MAXIMUM RATINGS ($T_j = 25^\circ\text{C}$, unless otherwise specified)****INVERTER PART**

Symbol	Parameter	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E Short	1200	V
V _{GES}	Gate-emitter voltage	C-E Short	±20	
I _C	Collector current	DC, $T_c = 105^\circ\text{C}$	(Note. 1)	A
I _{CRM}		Pulse	(Note. 4)	
P _C	Maximum collector dissipation	$T_c = 25^\circ\text{C}$	(Note. 1, 5)	W
I _E (Note.3)	Emitter current	$T_c = 25^\circ\text{C}$	(Note. 1)	35
I _{ERM} (Note.3)	(Free wheeling diode forward current)	Pulse	(Note. 4)	70

BRAKE PART

Symbol	Parameter	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E Short	1200	V
V _{GES}	Gate-emitter voltage	C-E Short	±20	
I _C	Collector current	DC, $T_c = 121^\circ\text{C}$	(Note. 1)	A
I _{CRM}		Pulse	(Note. 4)	
P _C	Maximum collector dissipation	$T_c = 25^\circ\text{C}$	(Note. 1, 5)	W
V _{RPM} (Note.3)	Repetitive peak reverse voltage		1200	V
I _F (Note.3)	Forward current	$T_c = 25^\circ\text{C}$	(Note. 1)	20
I _{FRM} (Note.3)		Pulse	(Note. 4)	40

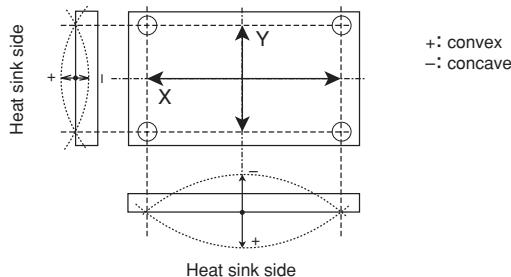
CONVERTER PART

Symbol	Parameter	Conditions	Rating	Unit
V _{RRM}	Repetitive peak reverse voltage		1600	V
E _a	Recommended AC input voltage		440	Vrms
I _O	DC output current	3-phase full wave rectifying, $T_c = 141^\circ\text{C}$ (Note. 1)	35	A
I _{FSM}	Surge forward current	The sine half wave 1 cycle peak value, $f = 60\text{Hz}$, non-repetitive	350	
I ² t	Current square time	Value for one cycle of surge current	510	A ² s

MODULE

Symbol	Parameter	Conditions	Rating	Unit
T _j	Junction temperature		-40 ~ +150	°C
T _{stg}	Storage temperature		-40 ~ +125	
V _{iso}	Isolation voltage	Terminals to base plate, $f = 60\text{Hz}$, AC 1 minute	2500	Vrms
—	Base plate flatness	On the centerline X, Y (Note. 8)	±0 ~ +100	μm
—	Torque strength	Mounting M5 screw	2.5 ~ 3.5	N·m
—	Weight	(Typical)	270	g

Note. 8: The base plate flatness measurement points are in the following figure.



+: convex
-: concave

HIGH POWER SWITCHING USE

ELECTRICAL and THERMAL RESISTANCE CHARACTERISTICS ($T_j = 25^\circ\text{C}$, unless otherwise specified)

INVERTER PART

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
ICES	Collector cutoff current	V _{CE} = V _{CES} , V _{GE} = 0V	—	—	1	mA
V _{GE(th)}	Gate-emitter threshold voltage	I _C = 3.5mA, V _{CE} = 10V	6	7	8	V
IGES	Gate leakage current	$\pm V_{GE} = V_{GES}$, V _{CE} = 0V	—	—	0.5	μA
V _{CE(sat)}	Collector-emitter saturation voltage	I _C = 35A, V _{GE} = 15V (Note. 6)	T _j = 25°C	—	2.0	2.6
			T _j = 125°C	—	2.2	—
		I _C = 35A, V _{GE} = 15V	Chip	—	1.9	—
C _{ies}	Input capacitance	V _{CE} = 10V V _{GE} = 0V (Note. 6)	—	—	6.0	nF
C _{oes}	Output capacitance		—	—	0.53	
C _{res}	Reverse transfer capacitance		—	—	0.12	
Q _G	Total gate charge	V _{CC} = 600V, I _C = 35A, V _{GE} = 15V	—	180	—	nC
t _{d(on)}	Turn-on delay time	V _{CC} = 600V, I _C = 35A	—	—	100	ns
t _r	Turn-on rise time	V _{GE} = $\pm 15\text{V}$, R _G = 9.1Ω	—	—	50	
t _{d(off)}	Turn-off delay time	Inductive load (I _E = 35A)	—	—	300	
t _f	Turn-off fall time		—	—	600	
t _{rr} (Note.3)	Reverse recovery time		—	—	200	
Q _{rr} (Note.3)	Reverse recovery charge		—	1.5	—	μC
V _{EC} (Note.3)	Emitter-collector voltage	I _E = 35A, V _{GE} = 0V (Note. 6)	T _j = 25°C	—	2.6	3.4
			T _j = 125°C	—	2.16	—
		I _E = 35A, V _{GE} = 0V	Chip	—	2.5	—
R _{th(j-c)Q}	Thermal resistance (Note. 1) (Junction to case)	per IGBT	—	—	0.42	K/W
R _{th(j-c)R}	per free wheeling diode	—	—	0.69		
R _{Gint}	Internal gate resistance	T _C = 25°C, per switch	—	0	—	Ω
R _G	External gate resistance	—	8.9	—	89	

BRAKE PART

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
ICES	Collector cutoff current	V _{CE} = V _{CES} , V _{GE} = 0V	—	—	1	mA
V _{GE(th)}	Gate-emitter threshold voltage	I _C = 2mA, V _{CE} = 10V	6	7	8	V
IGES	Gate leakage current	$\pm V_{GE} = V_{GES}$, V _{CE} = 0V	—	—	0.5	μA
V _{CE(sat)}	Collector-emitter saturation voltage	I _C = 20A, V _{GE} = 15V (Note. 6)	T _j = 25°C	—	2.0	2.6
			T _j = 125°C	—	2.2	—
		I _C = 20A, V _{GE} = 15V	Chip	—	1.9	—
C _{ies}	Input capacitance	V _{CE} = 10V V _{GE} = 0V (Note. 6)	—	—	5.1	nF
C _{oes}	Output capacitance		—	—	0.45	
C _{res}	Reverse transfer capacitance		—	—	0.1	
Q _G	Total gate charge	V _{CC} = 600V, I _C = 20A, V _{GE} = 15V	—	150	—	nC
I _{RRM} (Note.3)	Repetitive peak reverse current	V _R = V _{RRM}	—	—	1	mA
V _{FM} (Note.3)	Forward voltage drop	I _F = 20A (Note. 6)	T _j = 25°C	—	2.6	3.4
			T _j = 125°C	—	2.16	—
		I _F = 20A	Chip	—	2.5	—
R _{th(j-c)Q}	Thermal resistance (Note. 1) (Junction to case)	per IGBT	—	—	0.48	K/W
R _{th(j-c)R}	per Clamp diode	—	—	1.1		
R _{Gint}	Internal gate resistance	T _C = 25°C	—	0	—	Ω
R _G	External gate resistance	—	15	—	150	

CONVERTER PART

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
I _{RRM}	Repetitive peak reverse current	V _R = V _{RRM} , T _j = 150°C	—	—	4	mA
V _F	Forward voltage drop	I _F = 35A	—	1.2	1.6	V
R _{th(j-c)}	Thermal resistance (Note. 1) (Junction to case)	per Diode	—	—	0.45	K/W

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HIGH POWER SWITCHING USE

NTC THERMISTOR PART

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R	Zero power resistance	T _C = 25°C	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	T _C = 100°C, R ₁₀₀ = 493Ω	-7.3	—	+7.8	%
B(25/50)	B constant	Approximate by equation (Note. 7)	—	3375	—	K
P ₂₅	Power dissipation	T _C = 25°C	—	—	10	mW

MODULE

Symbol	Parameter	Conditions	Limits			Unit	
			Min.	Typ.	Max.		
R _{th(c-f)}	Contact thermal resistance (Case to fin) (Note. 1)	Thermal grease applied per 1 module	(Note. 2)	—	0.015	—	K/W

Note. 1: Case temperature (T_c), heat sink temperature (T_f) measured point is just under the chips. (Refer to the figure of the chip location.)

2: Typical value is measured by using thermally conductive grease of $\lambda = 0.9\text{W}/(\text{m}\cdot\text{K})$.

3: IE, IERM, VEC, trr, Qrr and Err represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).

If, IFRM, VF, VRMM and IRRM represent ratings and characteristics of the Clamp diode of Brake part.

4: Pulse width and repetition rate should be such that the device junction temperature (T_j) does not exceed T_{jmax} rating.

5: Junction temperature (T_j) should not increase beyond 150°C.

6: Pulse width and repetition rate should be such as to cause negligible temperature rise.

(Refer to the figure of the test circuit for $V_{CE(sat)}$ and V_{EC})

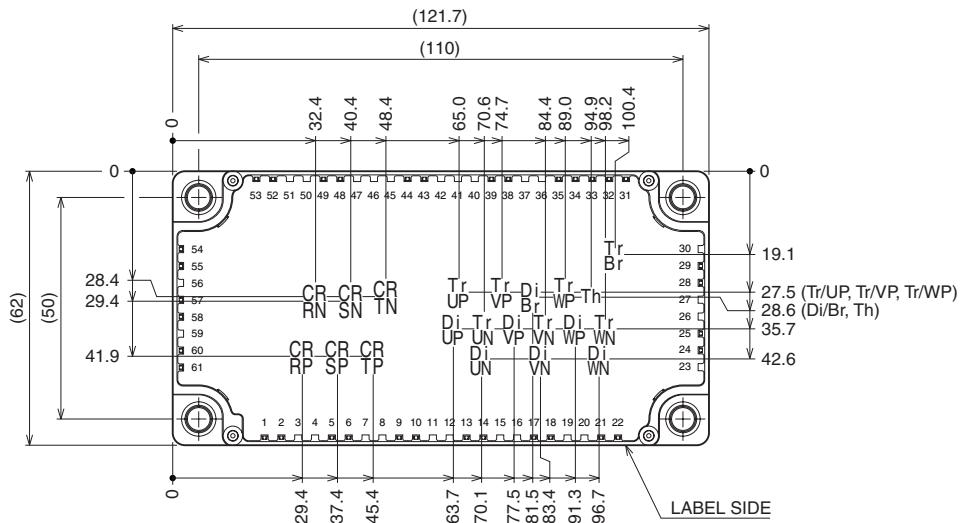
$$7: B_{(25/50)} = \ln\left(\frac{R_{25}}{R_{50}}\right) / \left(\frac{1}{T_{25}} - \frac{1}{T_{50}}\right)$$

R₂₅: resistance at absolute temperature T₂₅ [K]; T₂₅ = 25 [°C] + 273.15 = 298.15 [K]

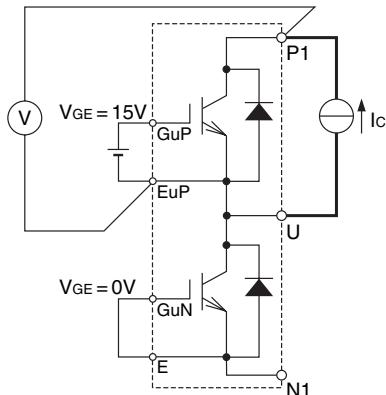
R₅₀: resistance at absolute temperature T₅₀ [K]; T₅₀ = 50 [°C] + 273.15 = 323.15 [K]

Chip Location (Top view)

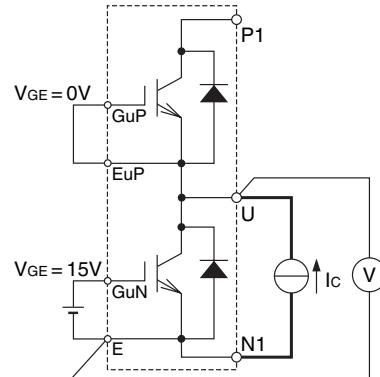
Dimensions in mm (tolerance: $\pm 1\text{mm}$)



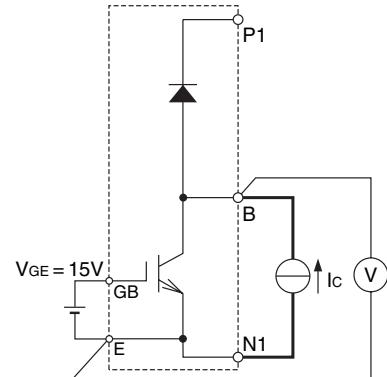
Each mark points the center position of each chip. Tr**: IGBT, Di**: FWDi (DiBr: Clamp diode), CR**: Converter diode, Th: NTC thermistor

HIGH POWER SWITCHING USE

P side Inverter part Tr
(example of U arm)
 $VG^*E^* = 0V$
(GvP-EvP, GwP-EwP, GvN-E, GwN-E, GB-E)

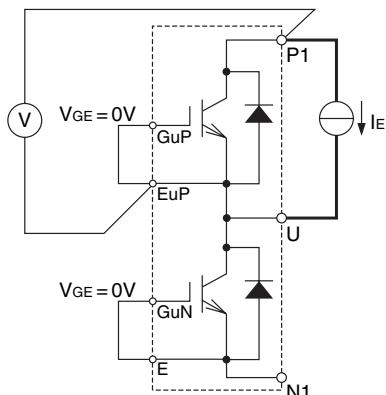


N side Inverter part Tr
(example of U arm)
 $VG^*E^* = 0V$
(GvP-EvP, GwP-EwP, GvN-E, GwN-E, GB-E)

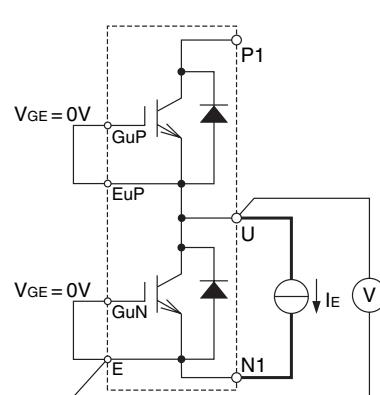


Br Tr
 $VG^*E^* = 0V$
(GuP-EuP, GvP-EvP, GwP-EwP,
GuN-E, GvN-E, GwN-E)

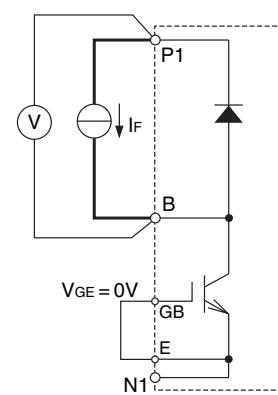
V_{CE(sat)} test circuit



P side Inverter part Di
(example of U arm)
 $VG^*E^* = 0V$
(GvP-EvP, GwP-EwP, GvN-E, GwN-E, GB-E)

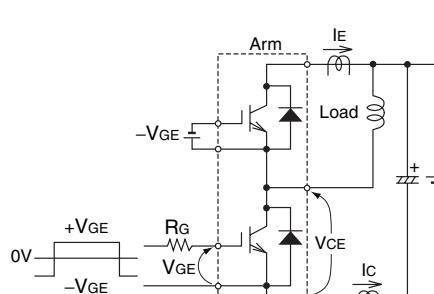


N side Inverter part Di
(example of U arm)
 $VG^*E^* = 0V$
(GvP-EvP, GwP-EwP, GvN-E, GwN-E, GB-E)

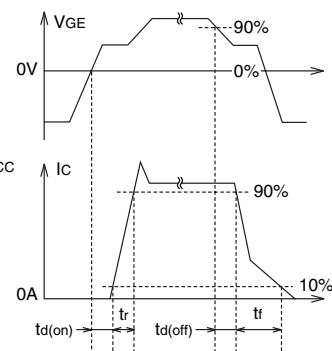


Br Di
 $VG^*E^* = 0V$
(GuP-EuP, GvP-EvP, GwP-EwP,
GuN-E, GvN-E, GwN-E)

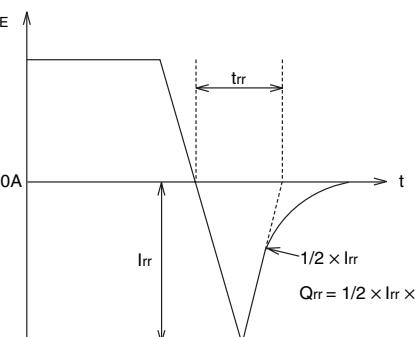
V_{CEc/VFM} test circuit



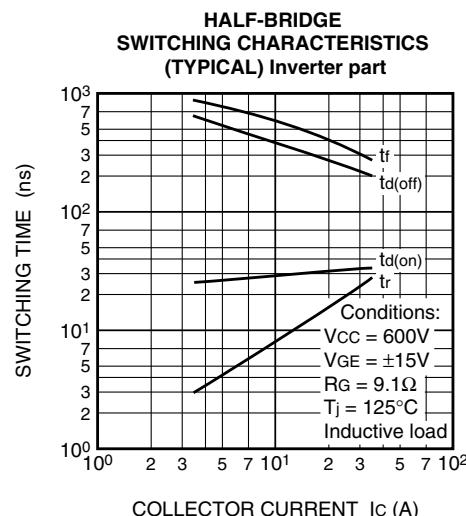
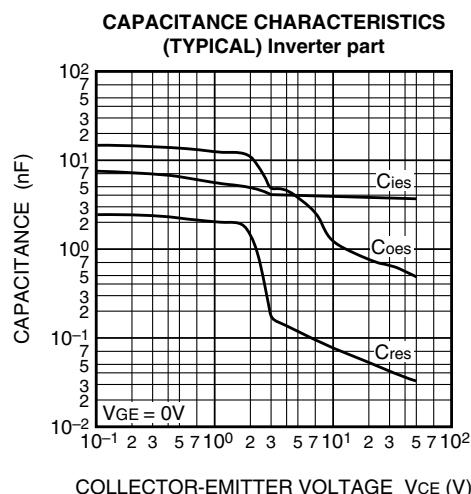
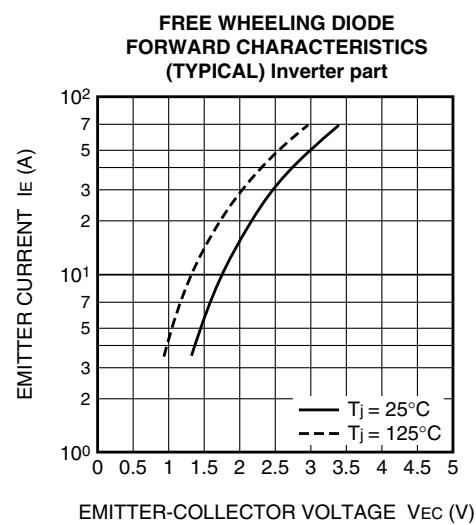
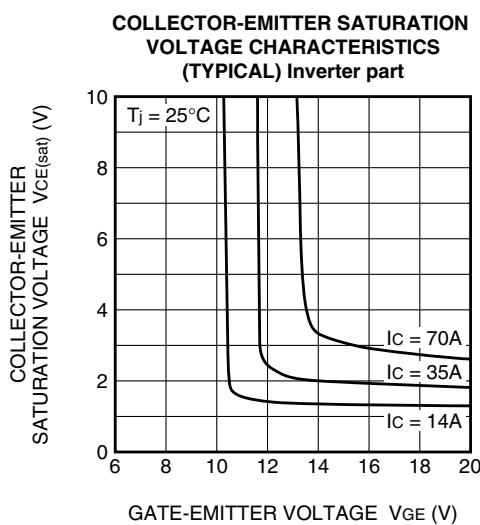
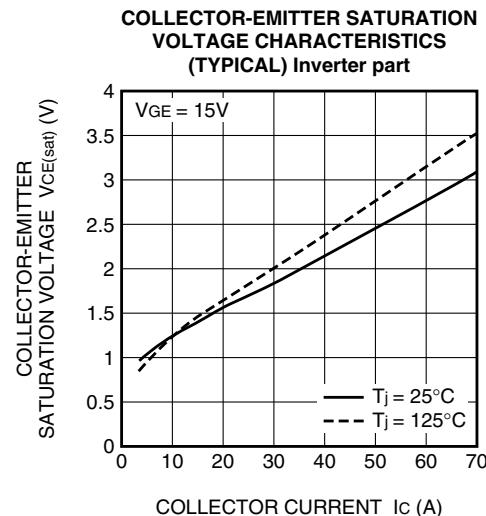
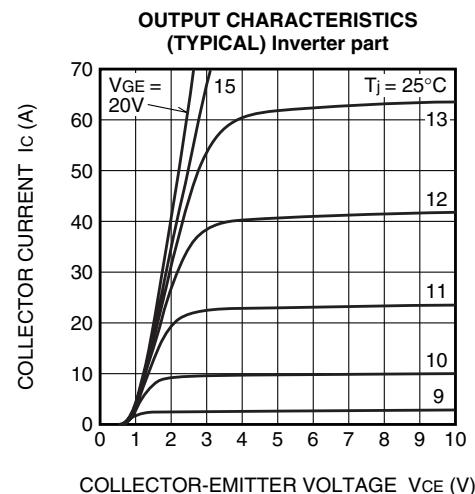
Switching time test circuit and waveforms



trr, Qrr test waveform



Jan. 2009

HIGH POWER SWITCHING USE**PERFORMANCE CURVES**

HIGH POWER SWITCHING USE